



Our Reference: DFS-138-A

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Martin Gehrke & Torsten Pechstein
Serial Number:	10/001,668
Filing Date:	October 23, 2001
Examiner/Art Group Unit:	Unknown/Unknown
Title:	A DESIGN FOR CONSTRUCTING AN INPUT CIRCUIT TO RECEIVE AND PROCESS AN ELECTRICAL SIGNAL

PATENT

SUBMITTAL OF PRIORITY DOCUMENT

Assistant Commissioner of Patents  
Washington, D.C. 20231

Sir:

The attorney for Applicant(s) respectfully submits, for filing with the  
subject patent application, the German priority document 100 52 532.6.

Respectfully submitted,

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WMH/jao

#5/Priority  
Papers  
9/14/02  
JHm

## ARTIFACT SHEET

Indicate quantity of item(s) received by the USPTO but not scanned.

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Priority Document No. 100 52 532-6

Country Deutschland

Doc Code: FRPR

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CD(s) containing computer program listing

Doc Code: Computer

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Stapled Set(s) of Extra Color Drawings

Doc Code: Artifact

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CD(s) containing pages of specification and/or sequence listing ☐

Doc Code: Artifact

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CD(s) with content unspecified

Doc Code: Artifact

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Microfilm(s)

Doc Code: Artifact

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Video tape(s)

Doc Code: Artifact

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Other mass storage media, description: \_\_\_\_\_

Doc Code: Artifact

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Model(s)

Doc Code: Artifact

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Other, description: \_\_\_\_\_

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Other, description: \_\_\_\_\_

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11/15/02